

19. (Currently Amended) An array substrate for an active matrix type liquid crystal display (LCD) device, comprising:

a pixel electrode formed over the passivation layer, said pixel electrode electrically connecting to said drain electrode via said drain contact hole, wherein said pixel electrode extends over a portion of said gate line so as to form a storage capacitor comprised of said pixel electrode, said gate line, and said first insulating layer, wherein said storage capacitor further includes a short-preventing part disposed between said pixel electrode and said gate line.

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21. (Original) The array substrate of claim 20, wherein said short-preventing part further includes an ohmic contact layer, and a conducting material between said semiconductor layer and said passivation layer.